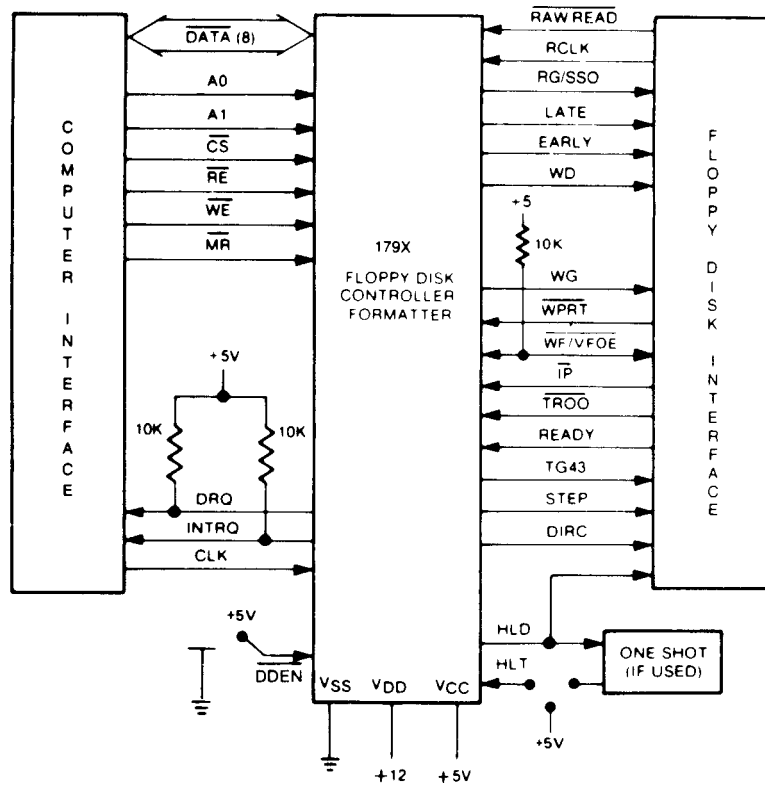


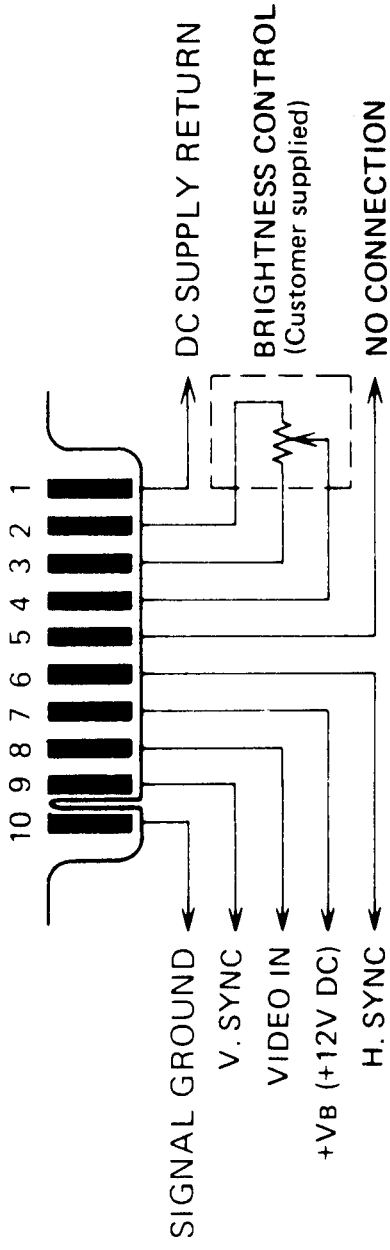
*1791/3 = RG 1795/7 = SSO
 **1793/7 TRUE BUS

PIN CONNECTIONS



FD 179 X

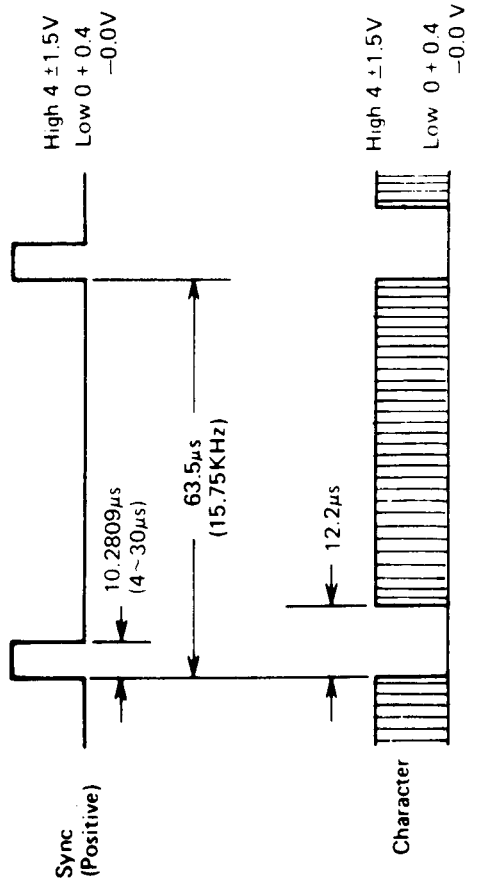
CONNECTOR WIRING



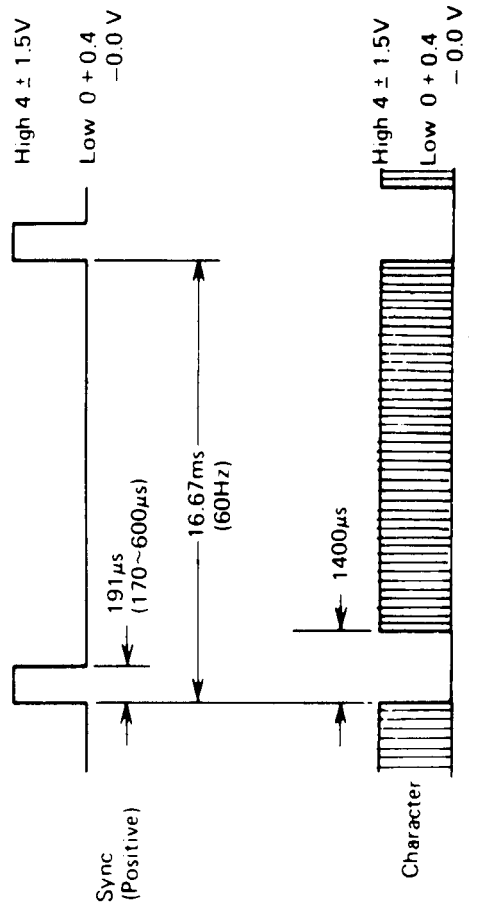
TIMING CHART

Note: Time Tolerance: $\pm 0.1\%$
 Sample unit is adjusted according to this timing and frequency.

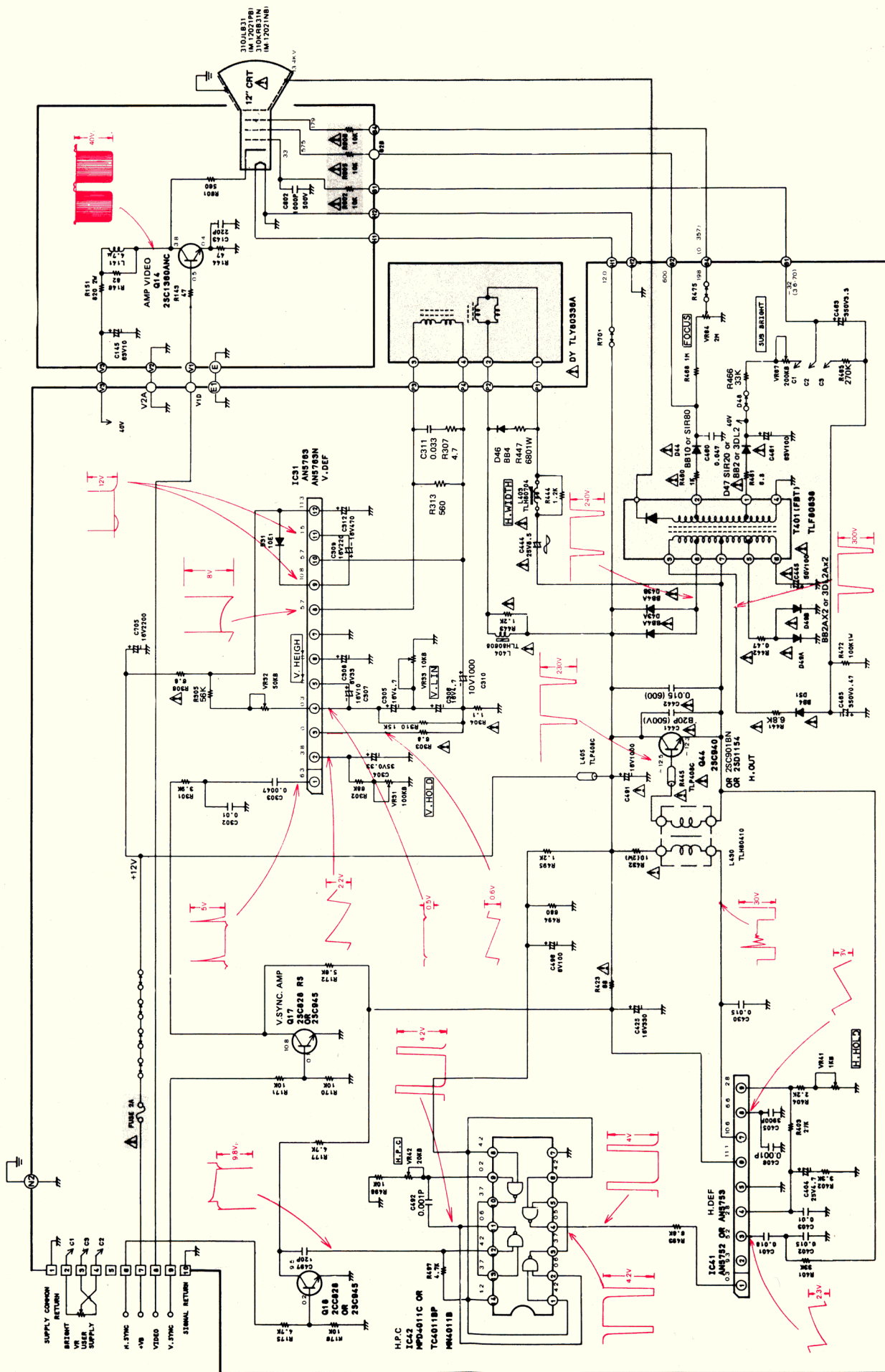
Horizontal Sync Timing



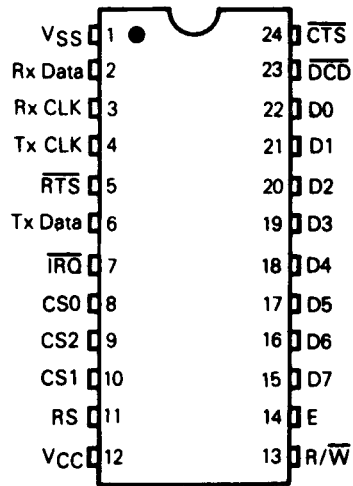
Vertical Sync Timing



SCHEMATIC DIAGRAM FOR MODELS M-12021PB/M-12021NB

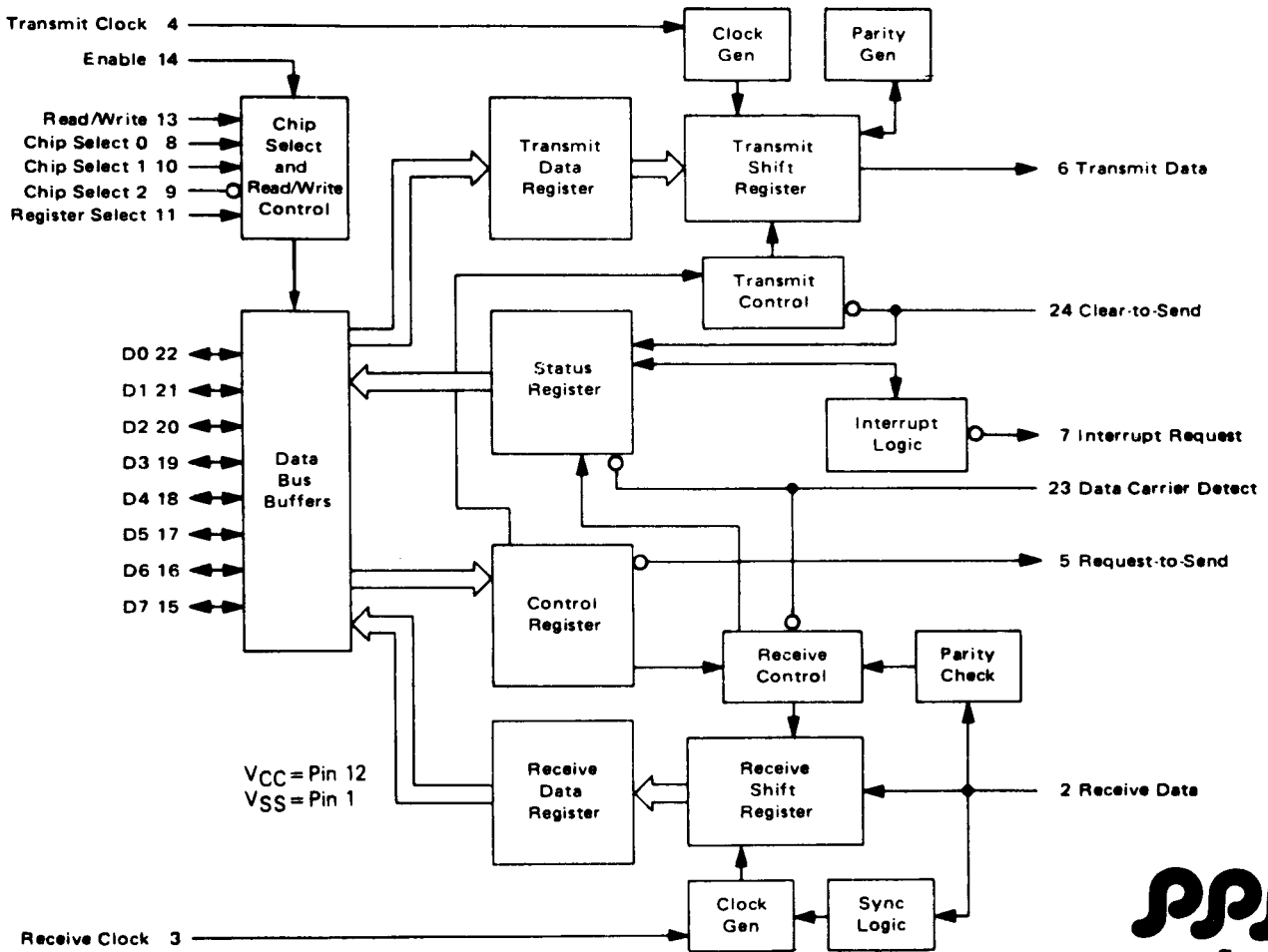


PIN ASSIGNMENT

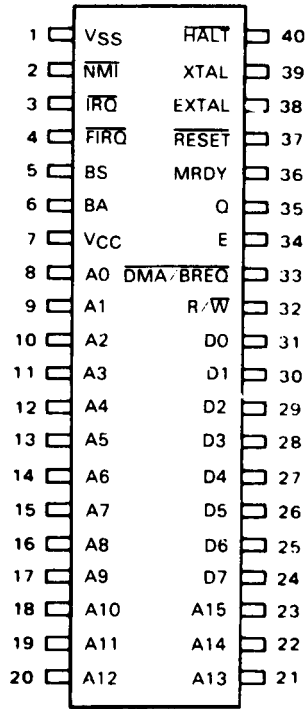


MC 68 A 50

EXPANDED BLOCK DIAGRAM

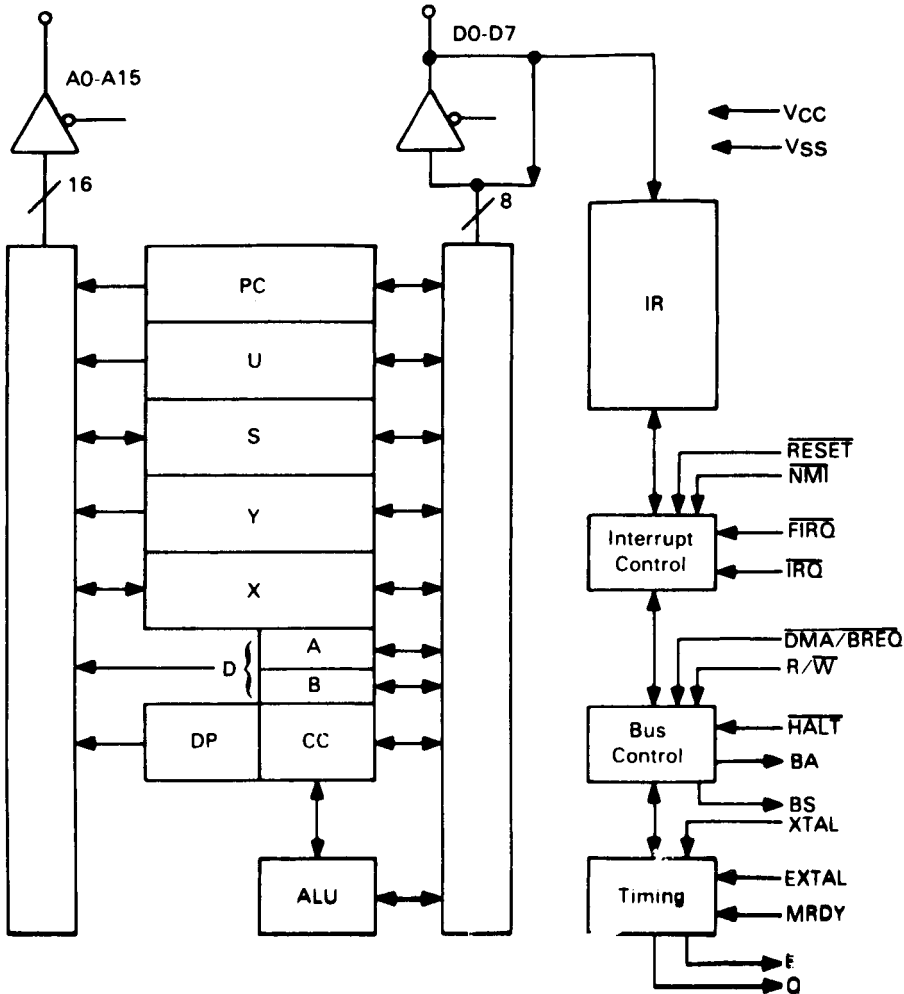


PIN ASSIGNMENT

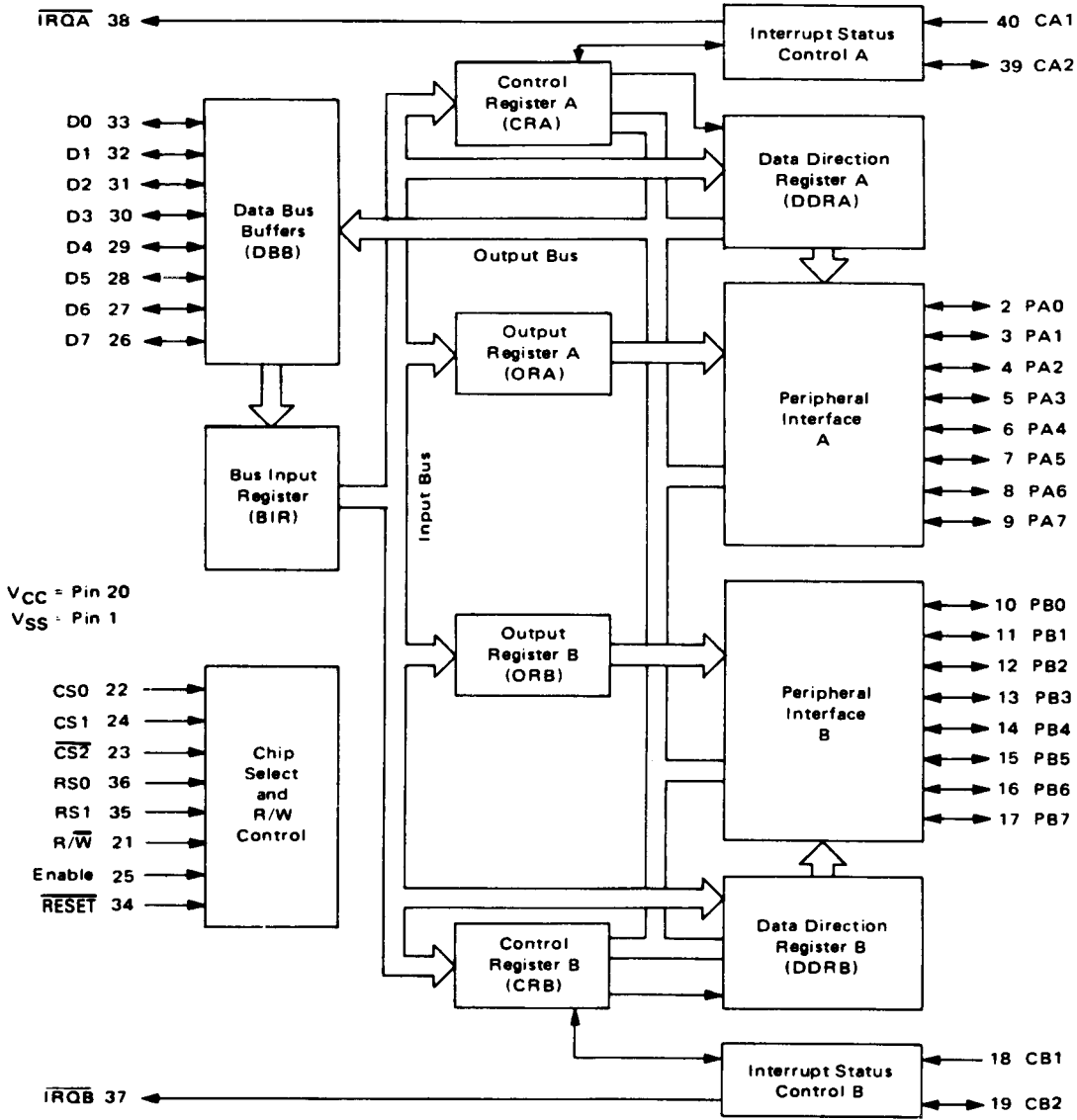


MC 68 A 09

MC6809 EXPANDED BLOCK DIAGRAM



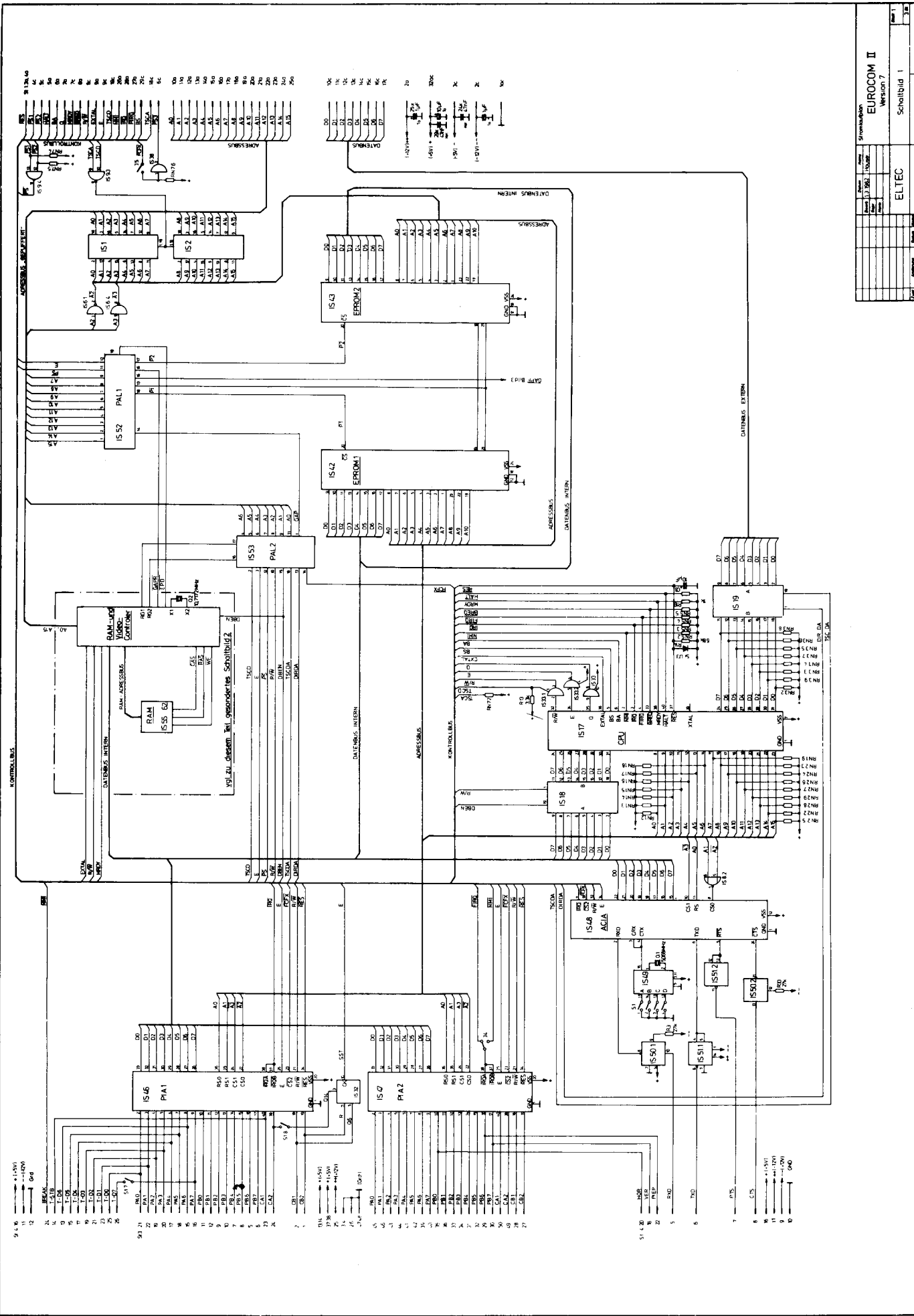
EXPANDED BLOCK DIAGRAM



PIN ASSIGNMENT

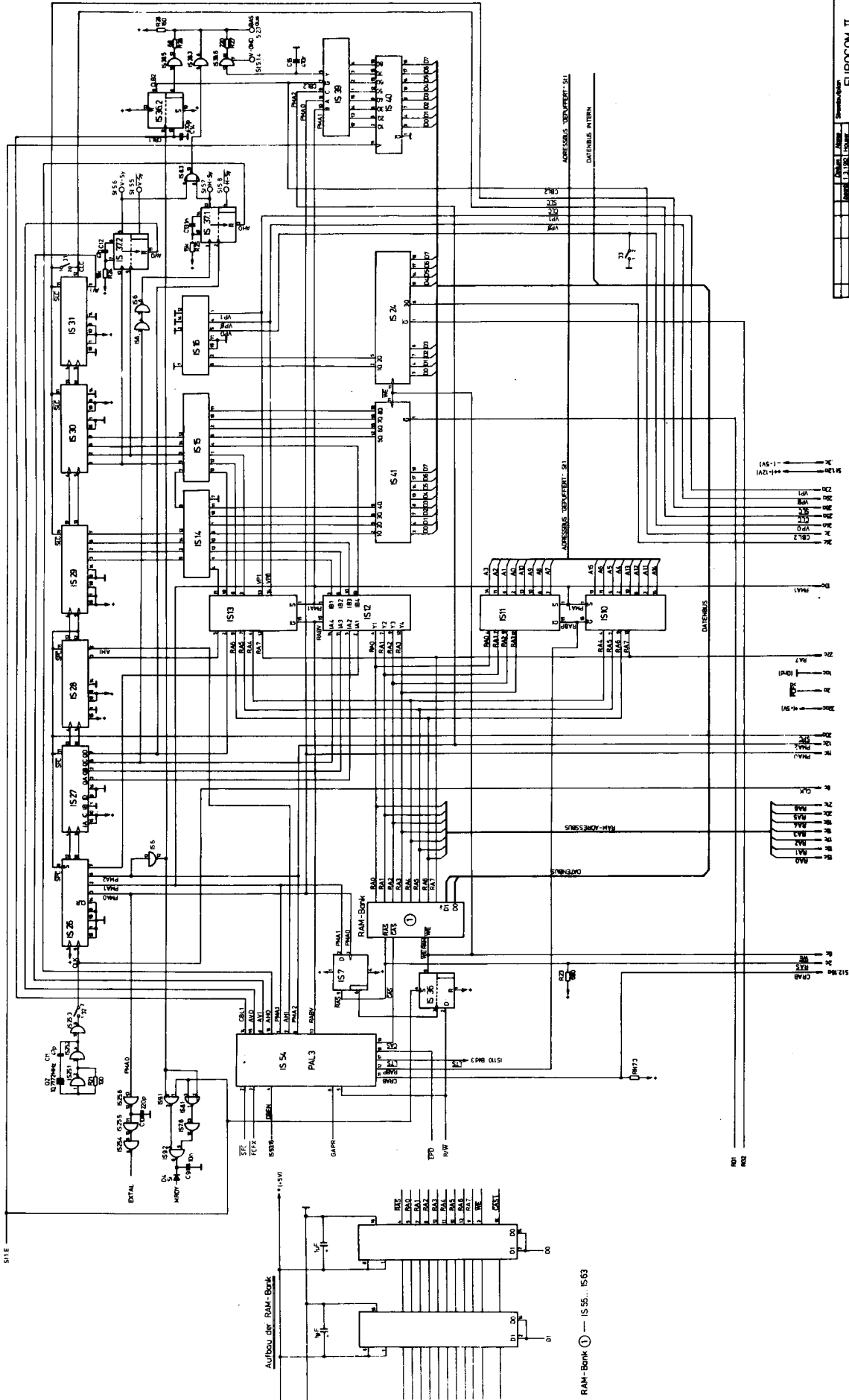
VSS	1	40	CA1
PA0	2	39	CA2
PA1	3	38	IRQA
PA2	4	37	IRQB
PA3	5	36	RS0
PA4	6	35	RS1
PA5	7	34	RESET
PA6	8	33	D0
PA7	9	32	D1
PB0	10	31	D2
PB1	11	30	D3
PB2	12	29	D4
PB3	13	28	D5
PB4	14	27	D6
PB5	15	26	D7
PB6	16	25	E
PB7	17	24	CS1
CB1	18	23	CS2
CB2	19	22	CS0
VCC	20	21	R/W

MC 68 A 21

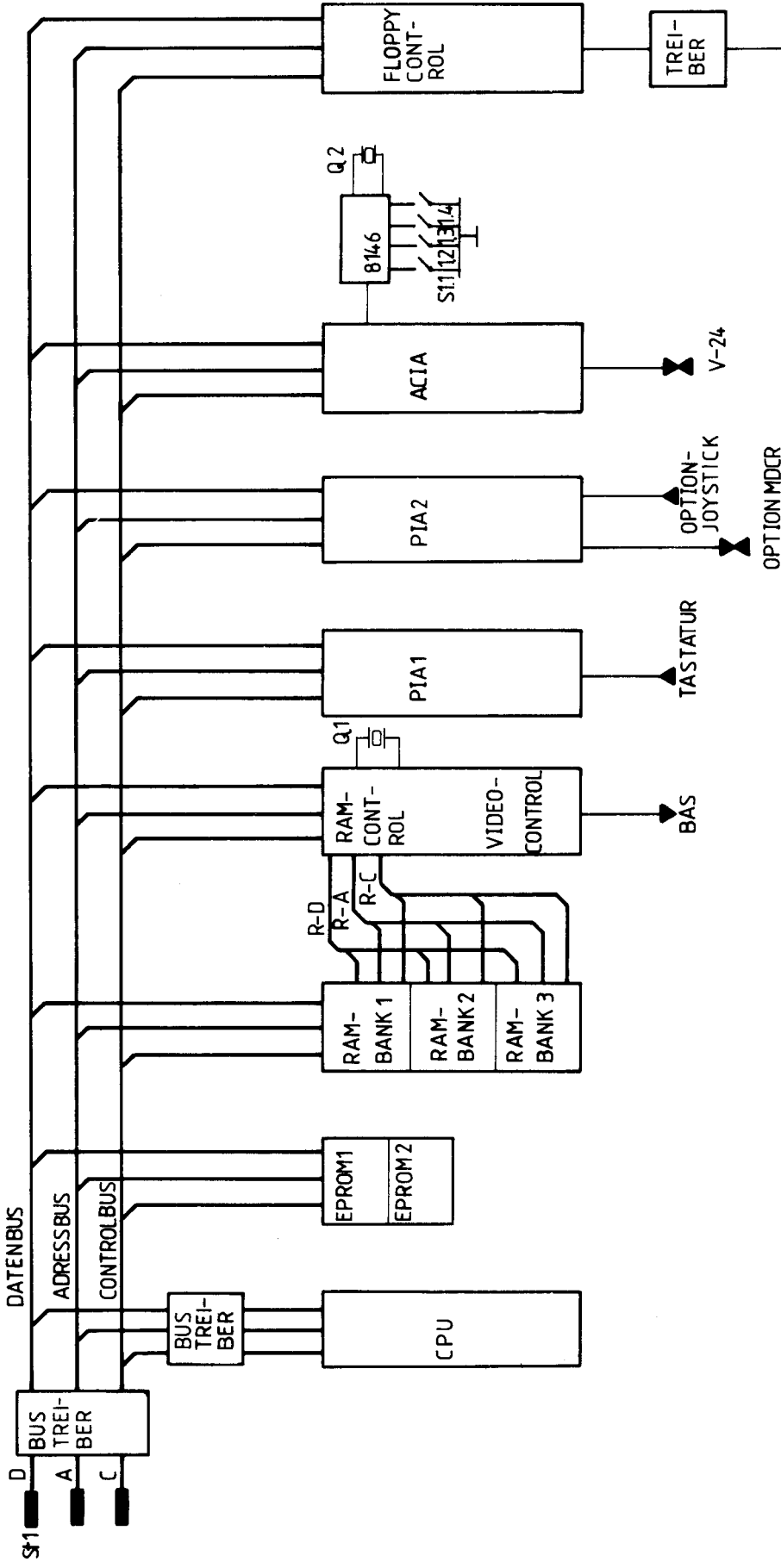


Rev.	1	2	3	4	5	6	7	8	9	10
Author										
Checked										
Approved										
Date										

EUROCOM II
 Version 7
 Schaltbild 1
 ELTEC



RAM-Bank 1 — IS 55... IS 63

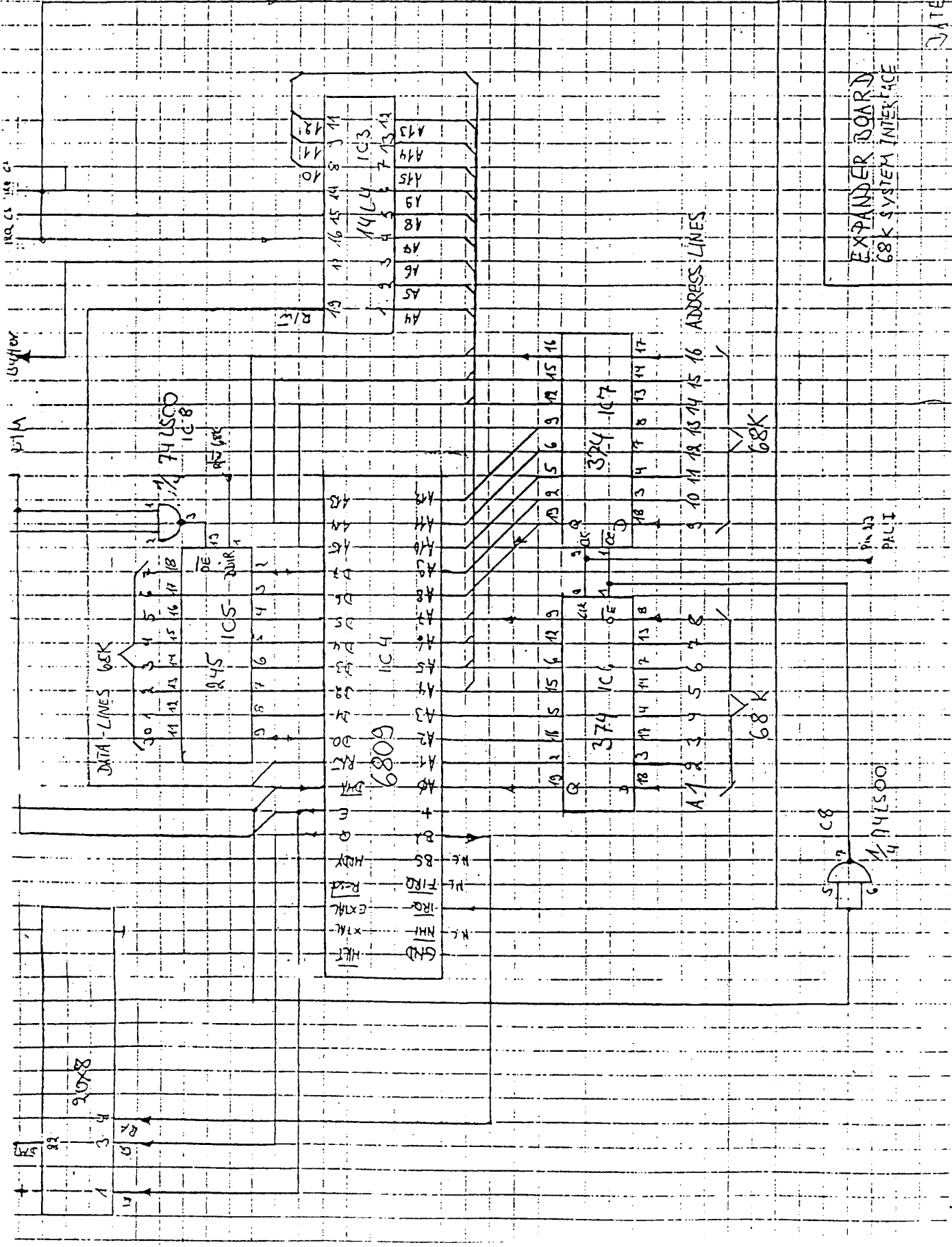


BIS 4 5 1/4" ODER 8"
FLOPPY DISKLAUFWERK

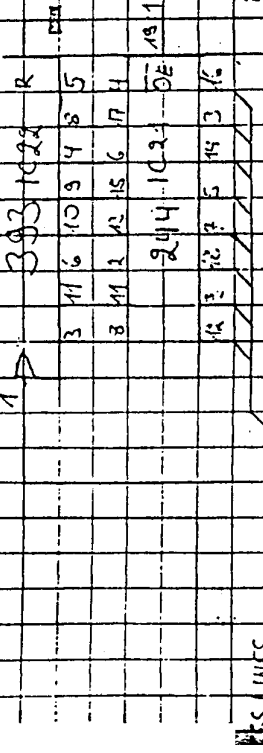


PINOUT FOR PRINTER AND KEYBOARD
ON THE WAVETERM

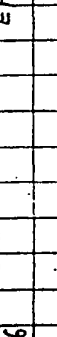
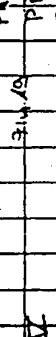
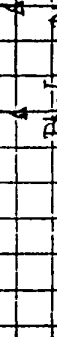
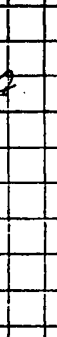
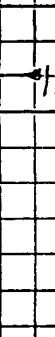
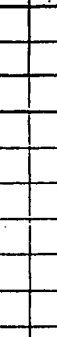
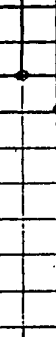
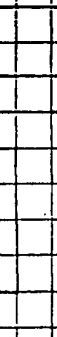
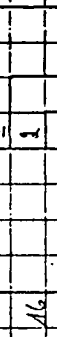
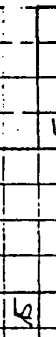
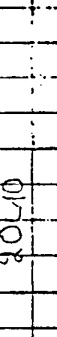
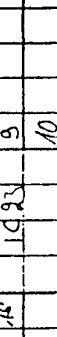
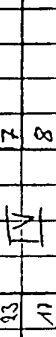
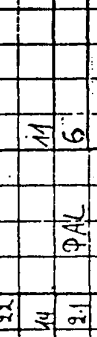
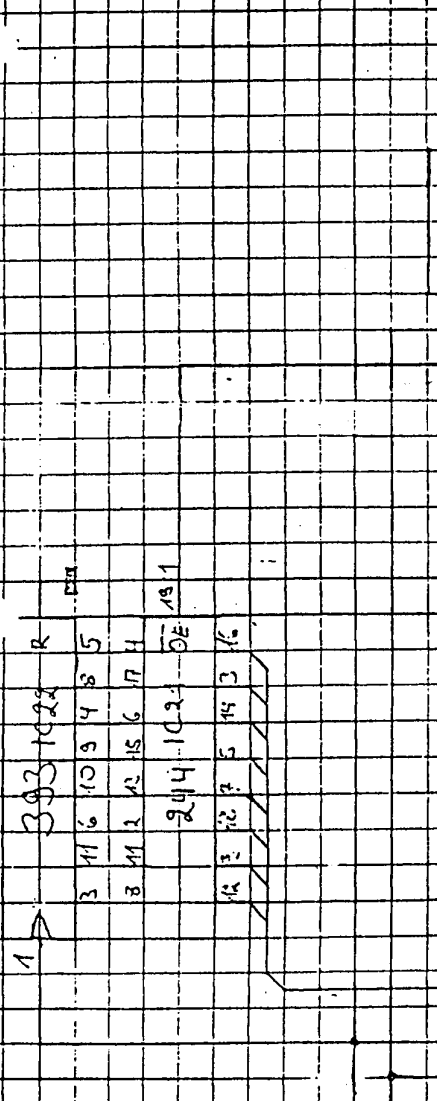
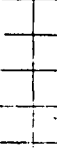
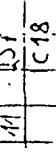
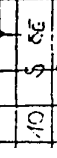
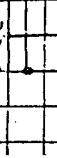
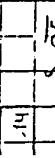
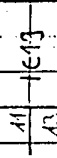
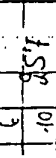
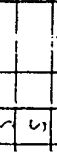
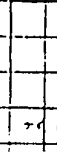
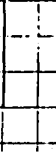
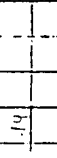
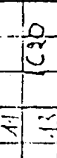
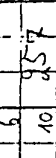
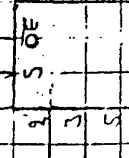
Pin Number	Function	
	Printer	Keyboard
1	+5V	+5V
2	D7	(D7)
3	D5	D5
4	D3	D3
5	D1	D1
6	ground	-----
7	busy	ground
8	D6	D6
9	D4	D4
10	D2	D2
11	D0	D0
12	ground	strobe
13	strobe	(+12V)
14	-----	-----



EXPANDER BOARD
68K SYSTEM INTERFACE
PAC
DATE: 1/1/71

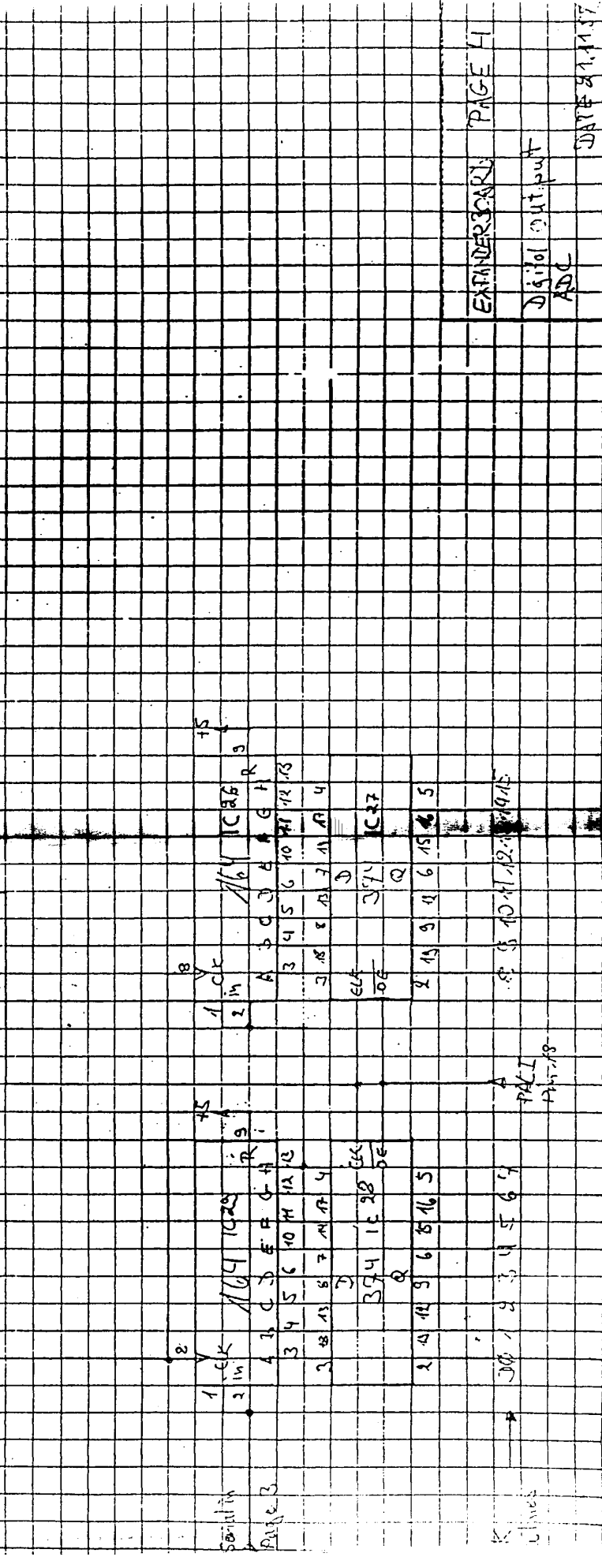
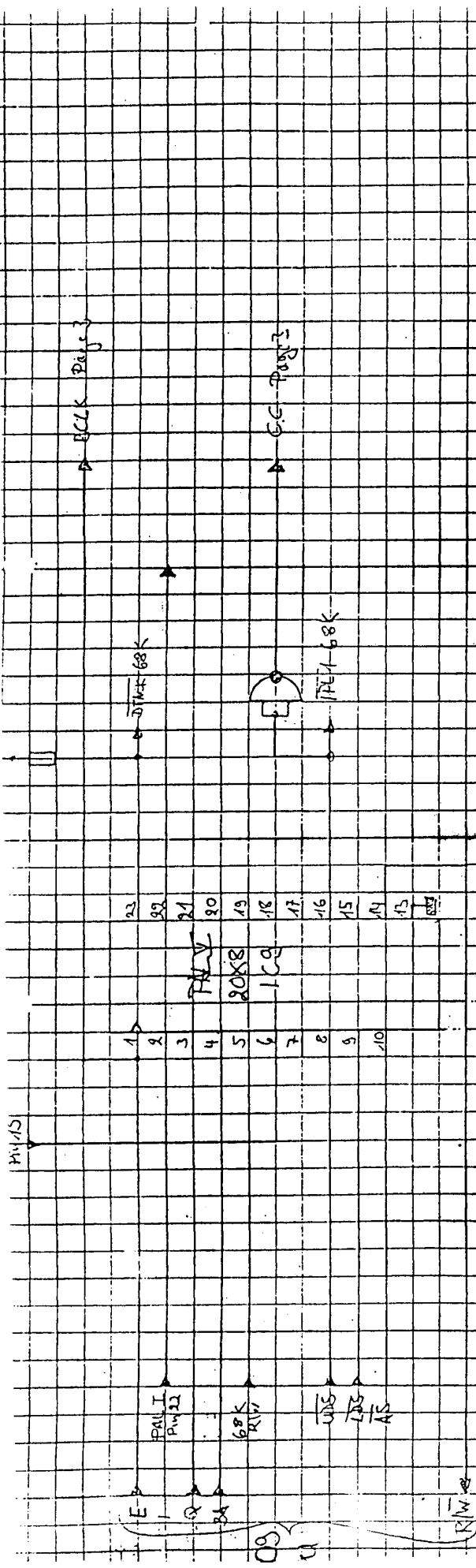


RESLINES

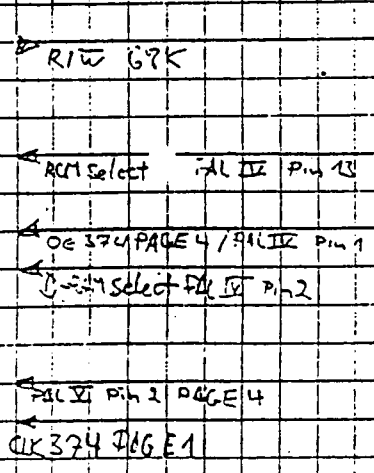
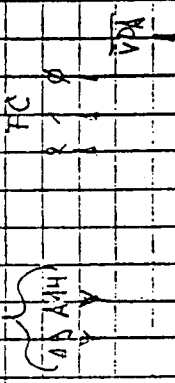


EXPANDER (WARD)
PAGE 2
D-RAM and Refresh circuit

DATE 1/11/85



Address lines



21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
----	----	----	----	----	----	----	----	----	----	----	----	---	---	---	---	---	---	---	---	---	---

PAGE I 20C1016-11

PAGE II 20C1016-11

21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
----	----	----	----	----	----	----	----	----	----	----	----	---	---	---	---	---	---	---	---	---	---

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
---	---	---	---	---	---	---	---	---	----	----	----	----	----	----

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
----	----	----	----	----	----	---	---	---	---	---	---	---	---	---	---

15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
----	----	----	----	----	----	---	---	---	---	---	---	---	---	---	---

ADDRESS LINES

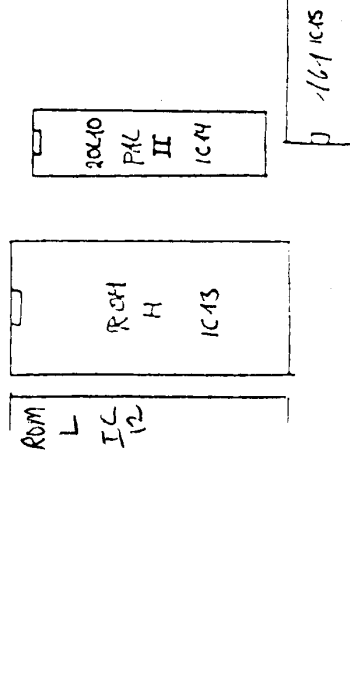
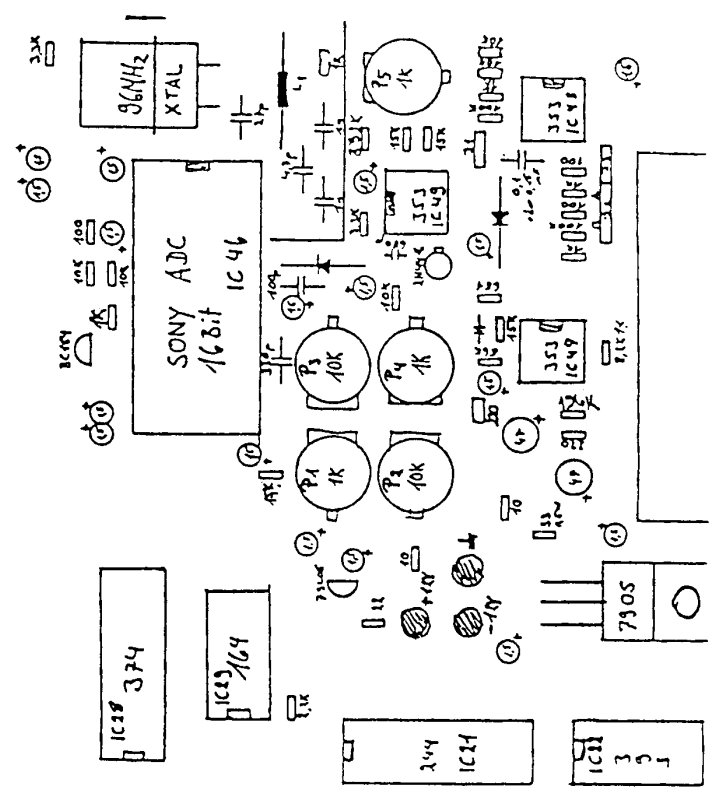
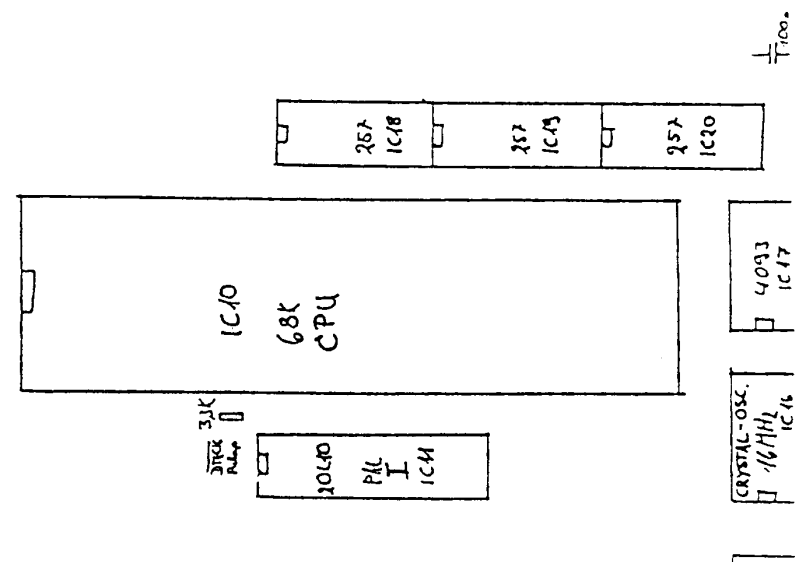
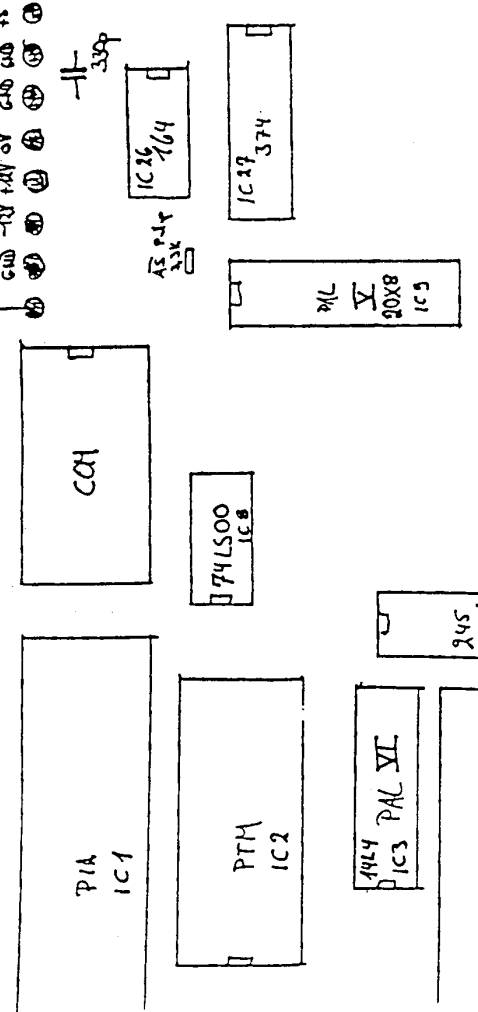
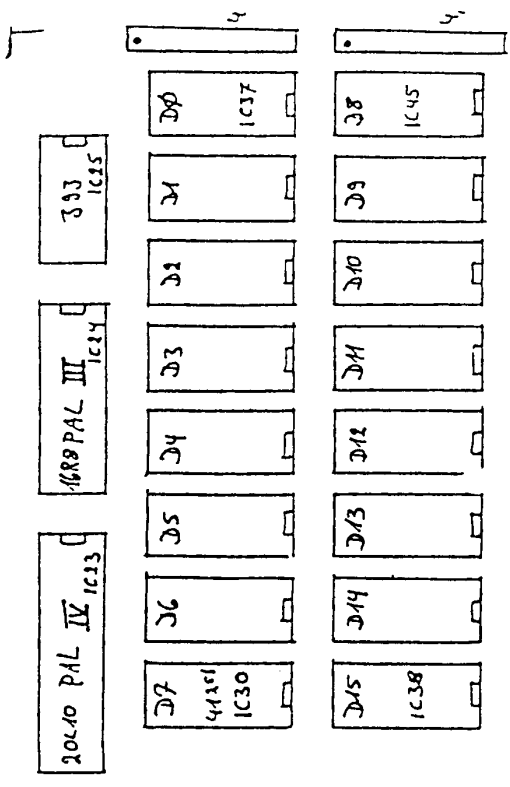
EXPANDER BOARD PAGE 5

Address: deca00000000

DATE: 2/1/11

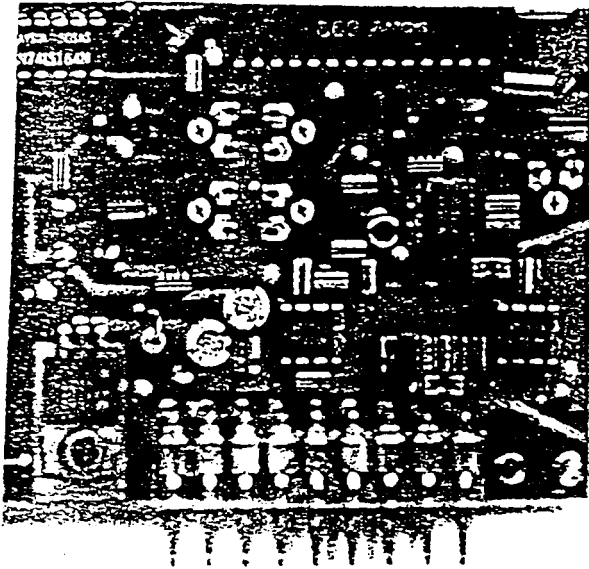
supply for Analog

supply for Digital



1200 Terminator B
16-bit Exp. board
Jumper settings:

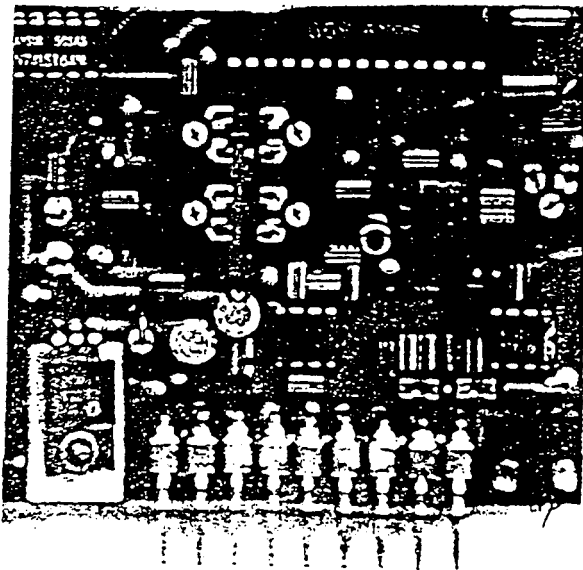
1. 16-bit / M...



00
Jumper 2

0000
Jumper 1

2. 16-bit only



00 not used!

10010101 Jumper 2
Jumper 1